

Fig. 1
(prior art)

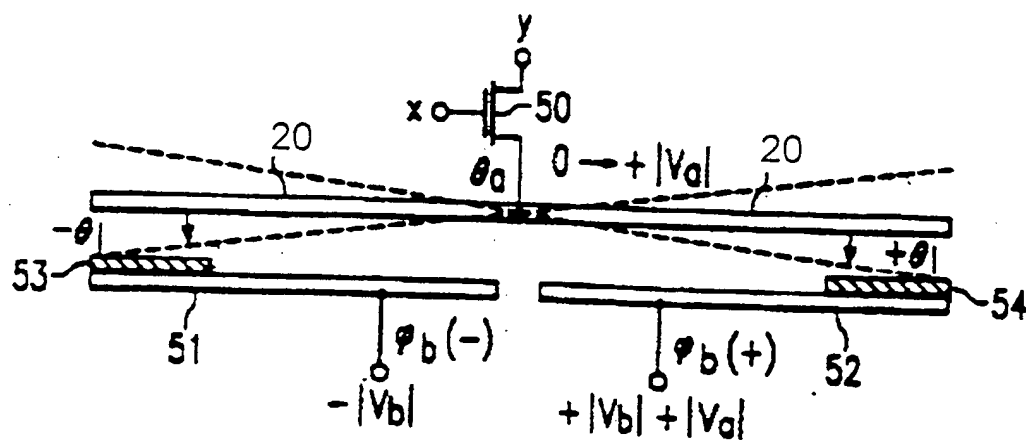


Fig. 2a
(prior art)

10034896 12201
"9684E00T"

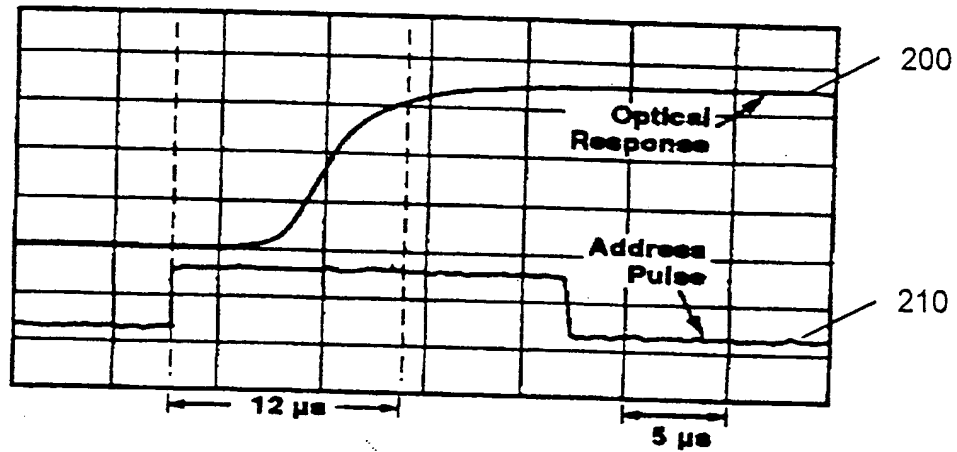


Fig. 2b (prior art)

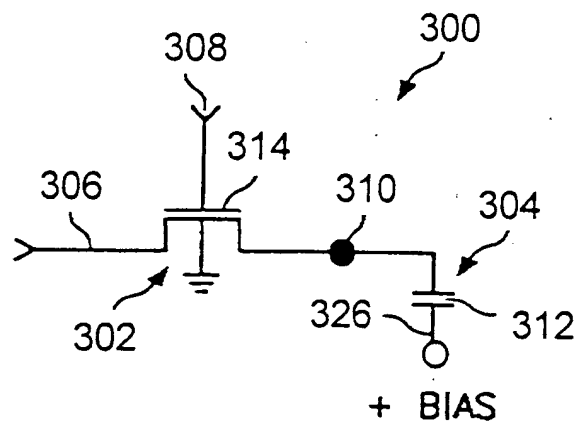


Fig. 4a (prior art)

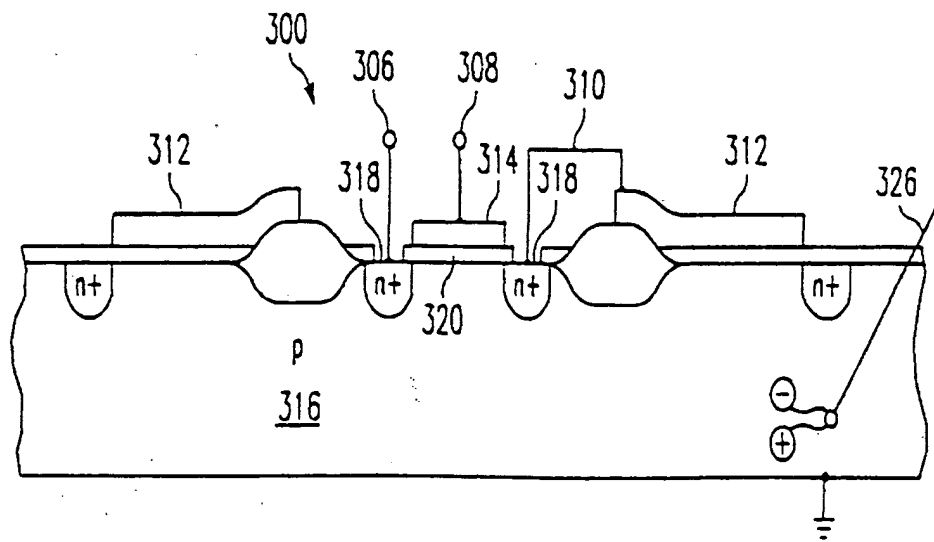


Fig. 3 (prior art)

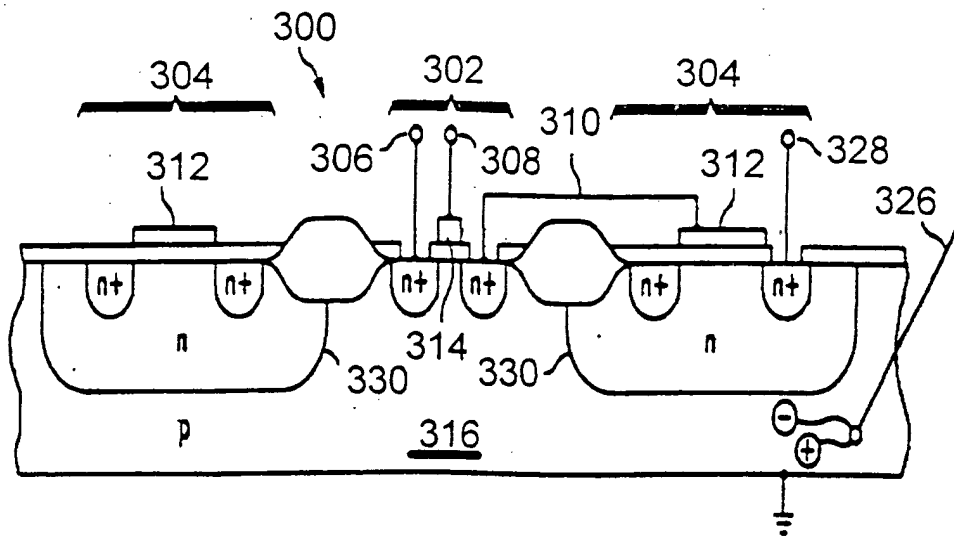


Fig. 4b (prior art)

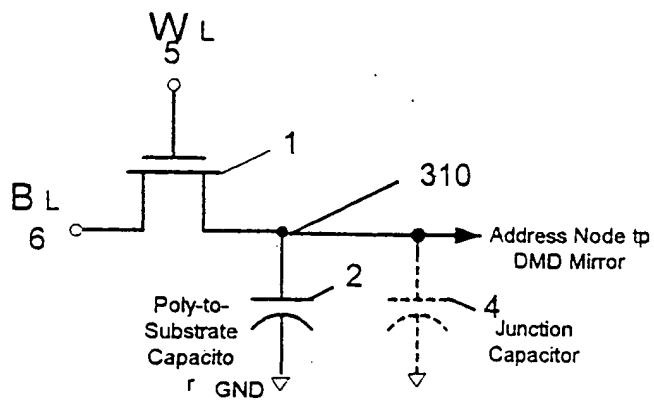


Fig. 5a

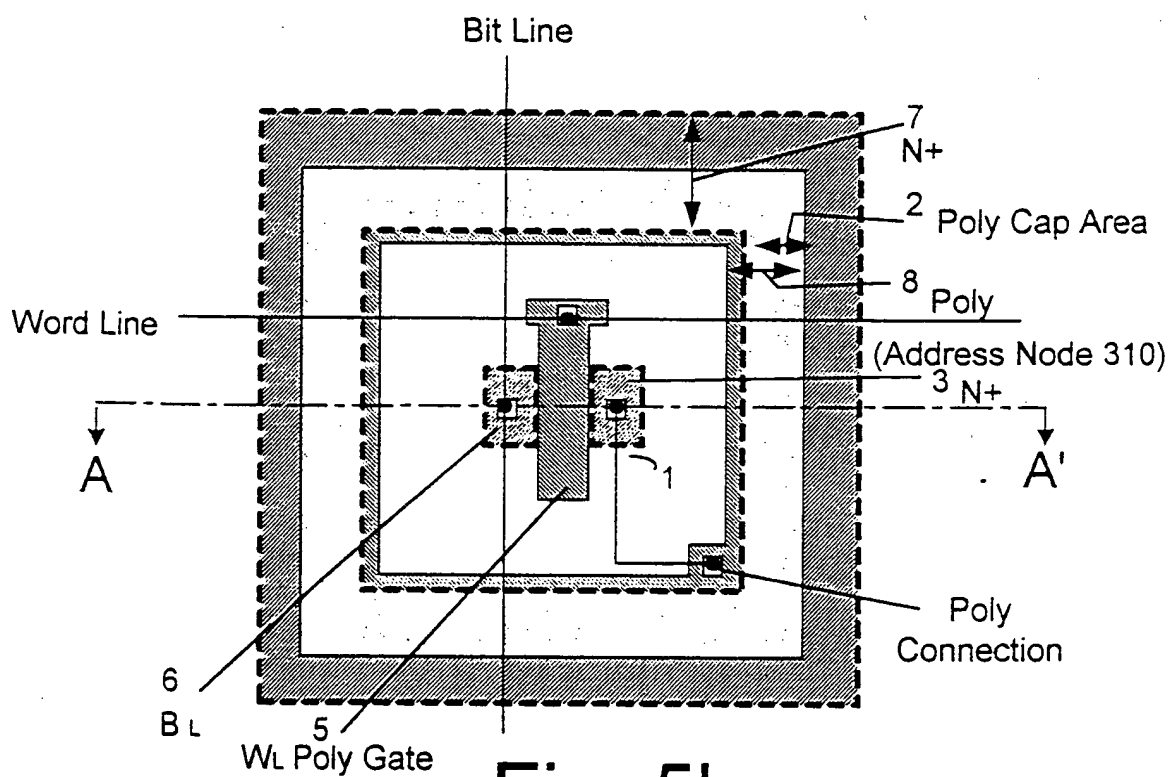


Fig. 5b

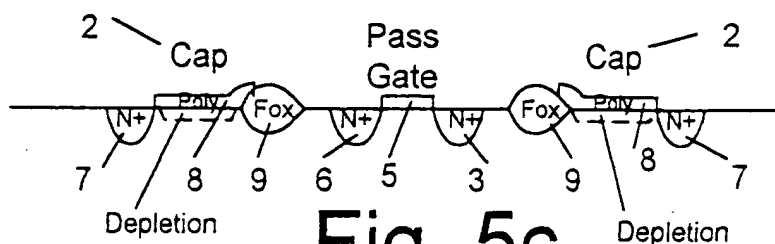


Fig. 5c

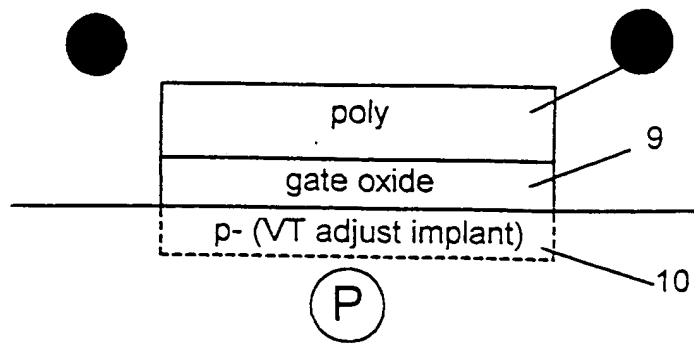


Fig. 6a

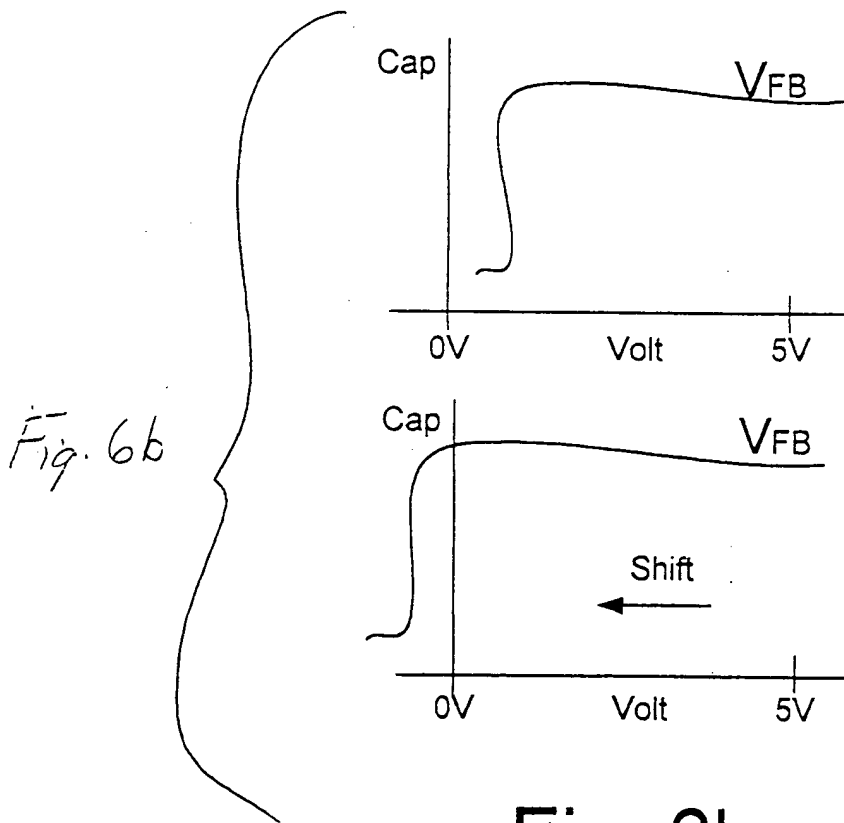
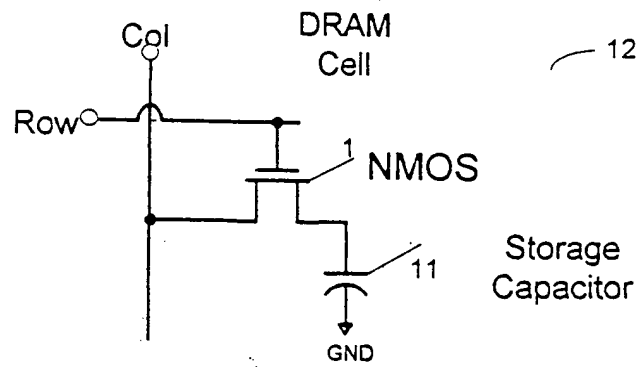
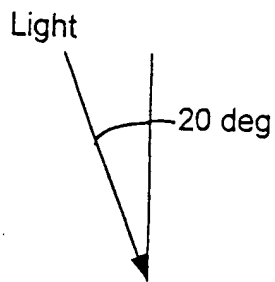
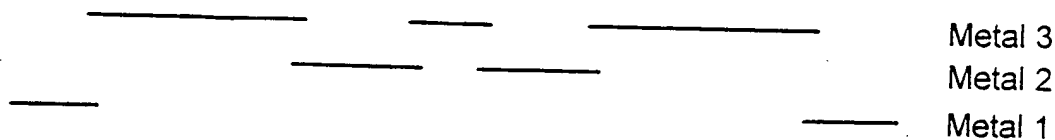


Fig. 6b





(Metal coverage of DRAM CELL)



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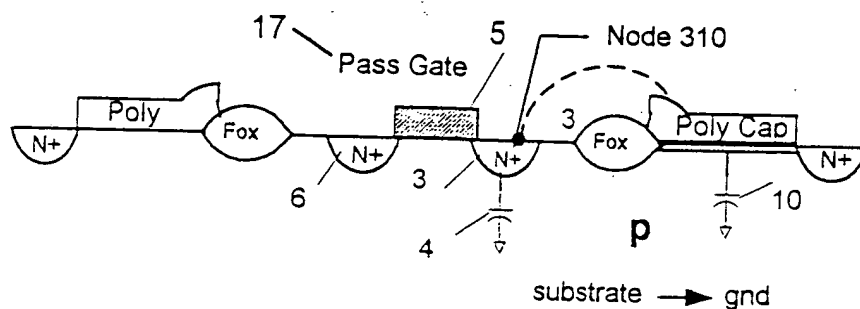


Fig.8A

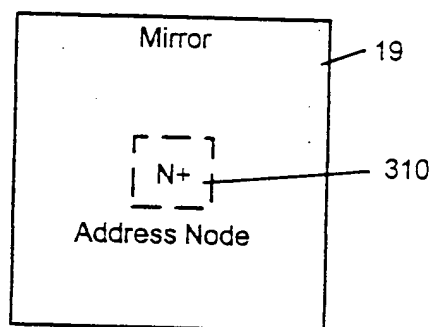


Fig. 8B

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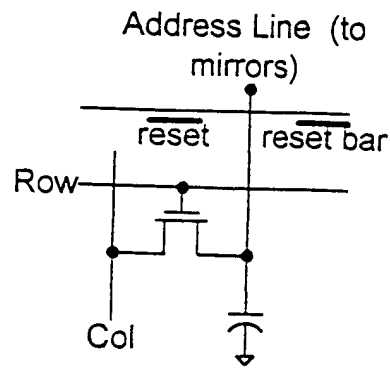


Fig. 9a

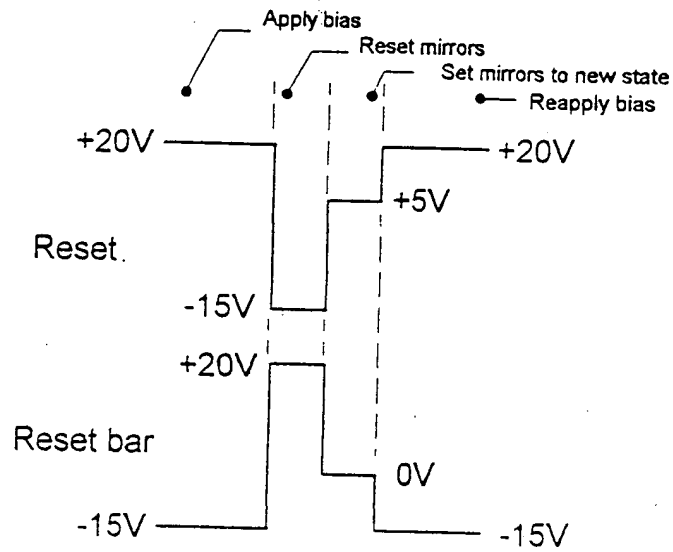


Fig. 9b

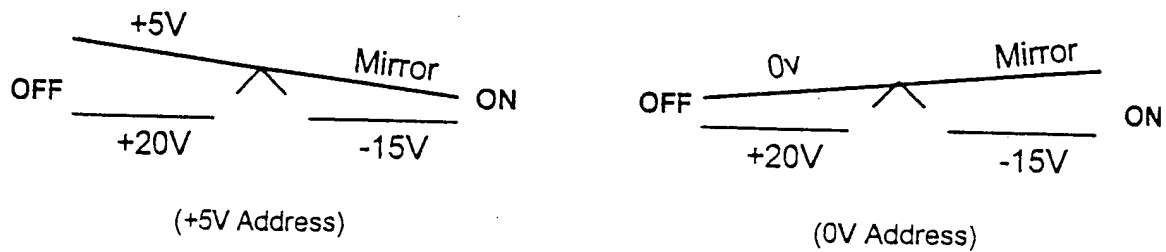


Fig. 9c

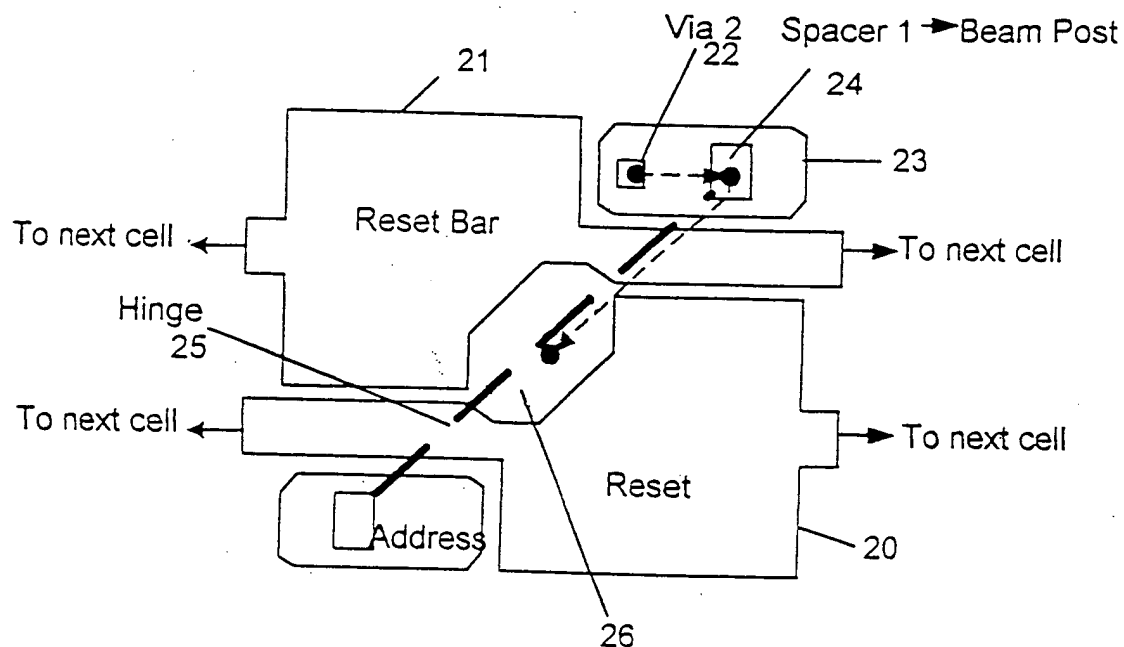


Fig. 10a

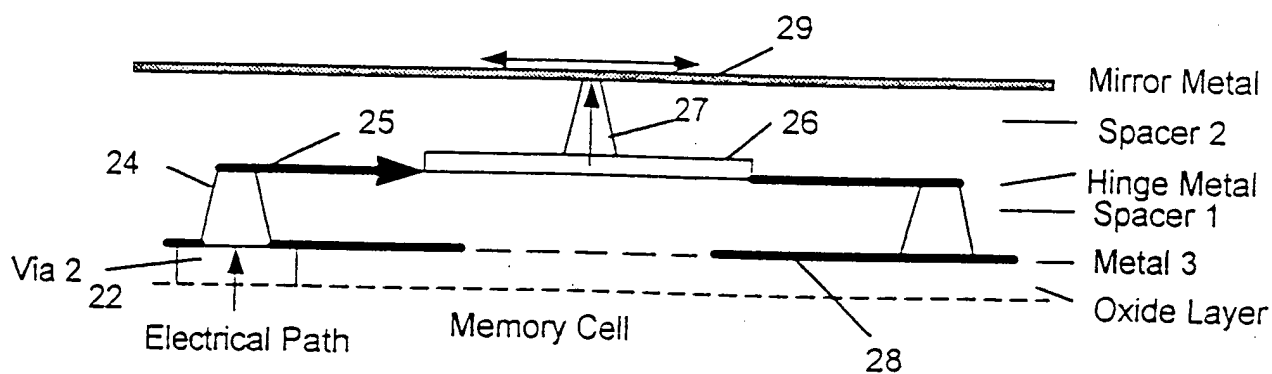


Fig. 10b

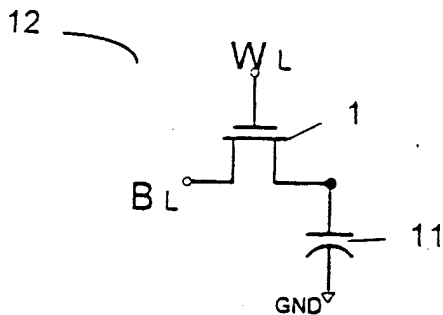


Fig. 11a

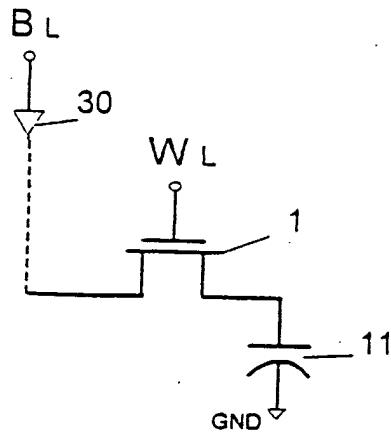
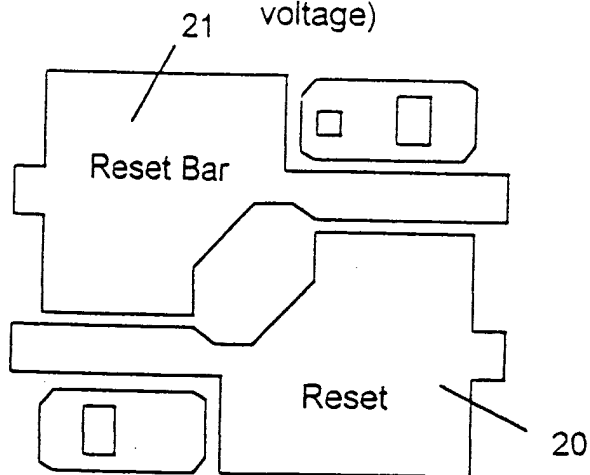


Fig. 11b

(Electrostatic member which attracts Yoke to ON side for +5V address voltage)



(Electrostatic member which attracts Yoke to OFF side for 0V address voltage)

Fig. 11c

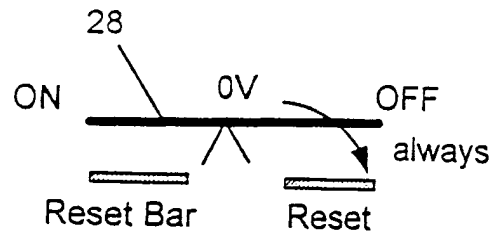


Fig. 11D

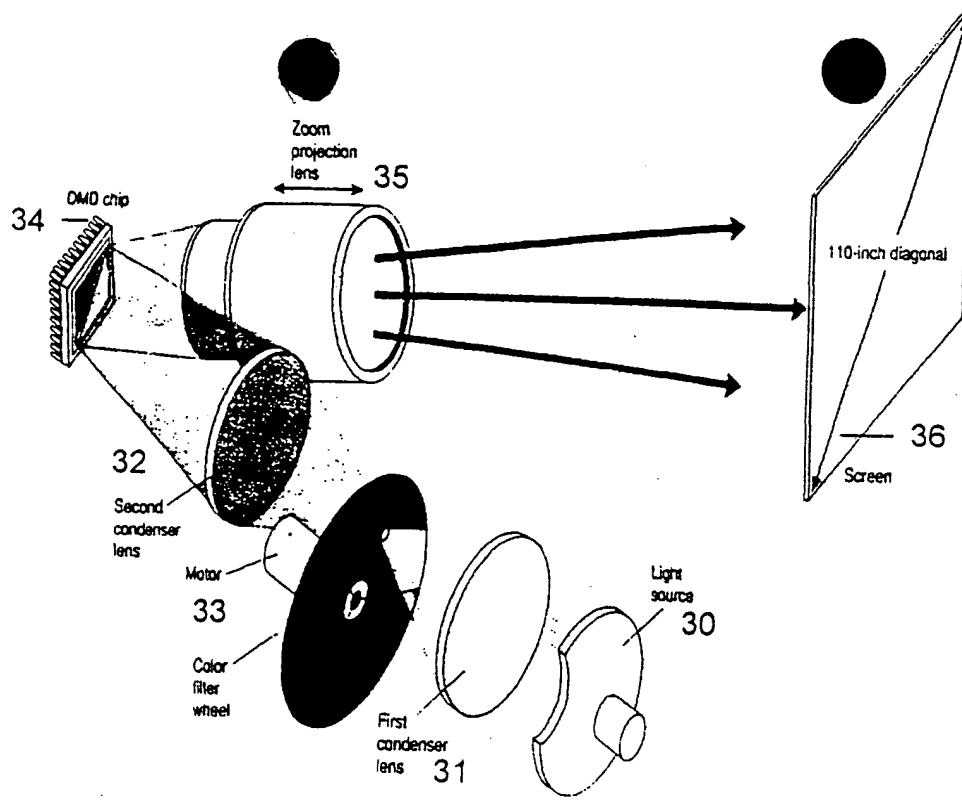


Fig. 12a

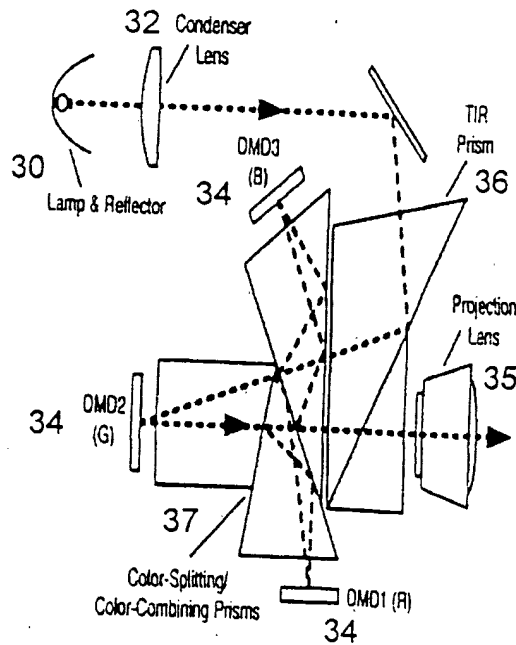
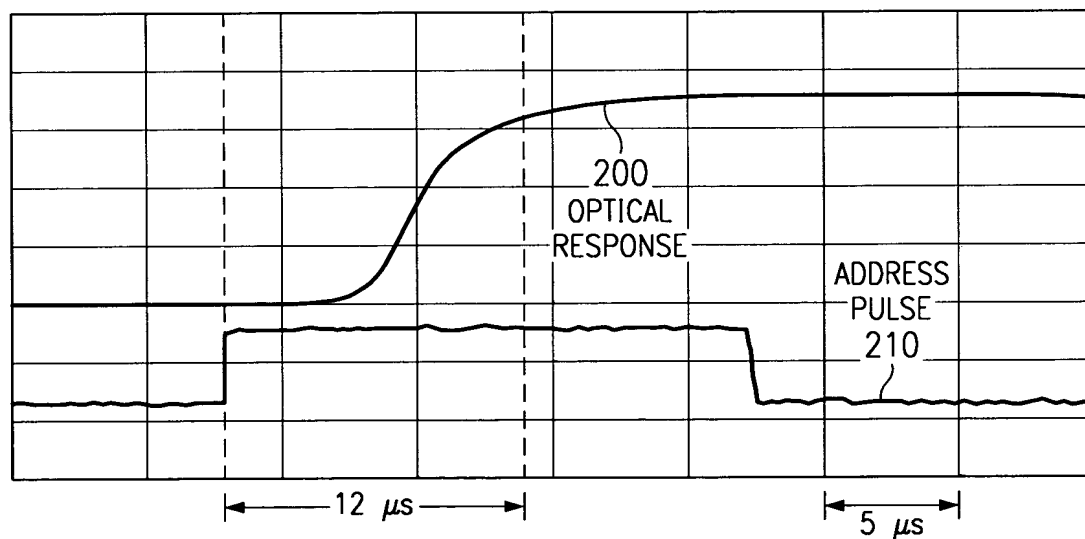
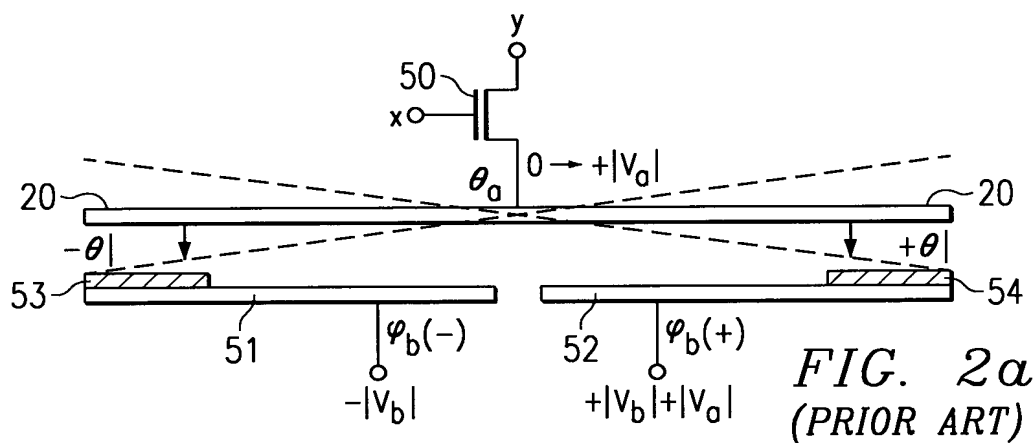
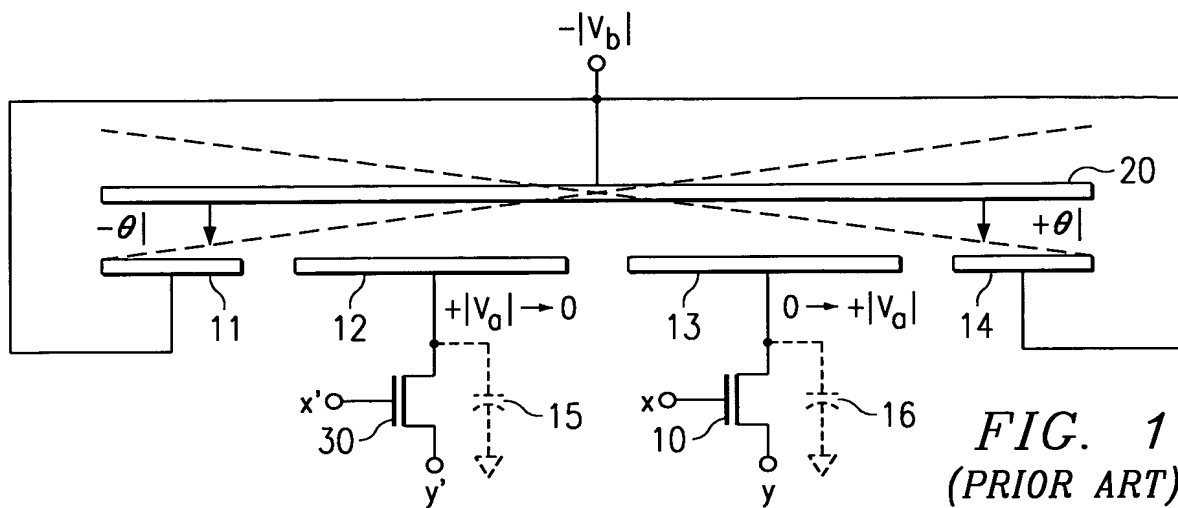


Fig. 12b



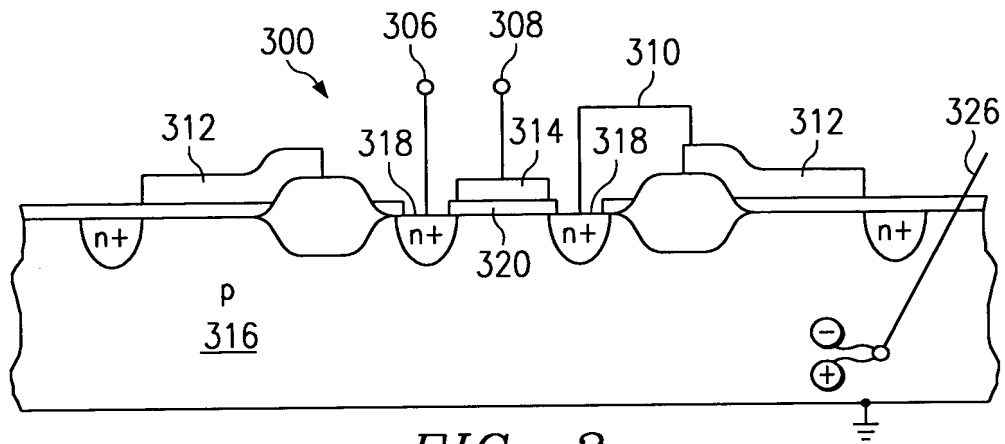


FIG. 3
(PRIOR ART)

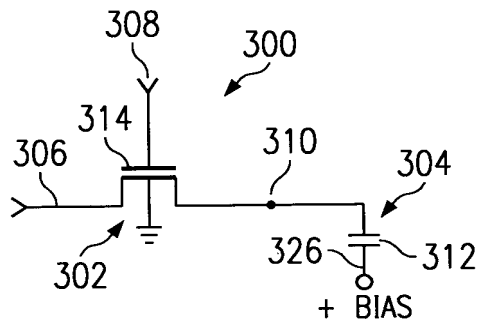


FIG. 4a
(PRIOR ART)

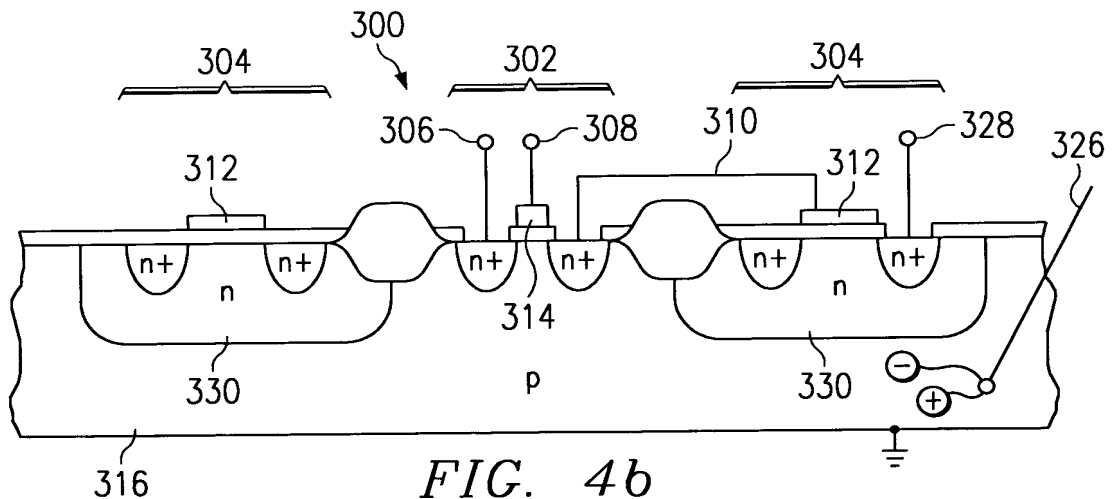


FIG. 4b
(PRIOR ART)

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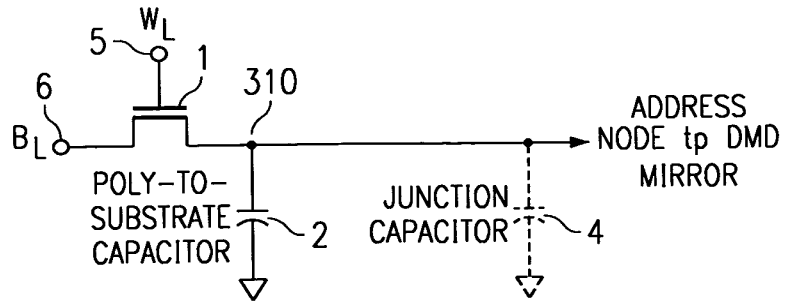


FIG. 5a

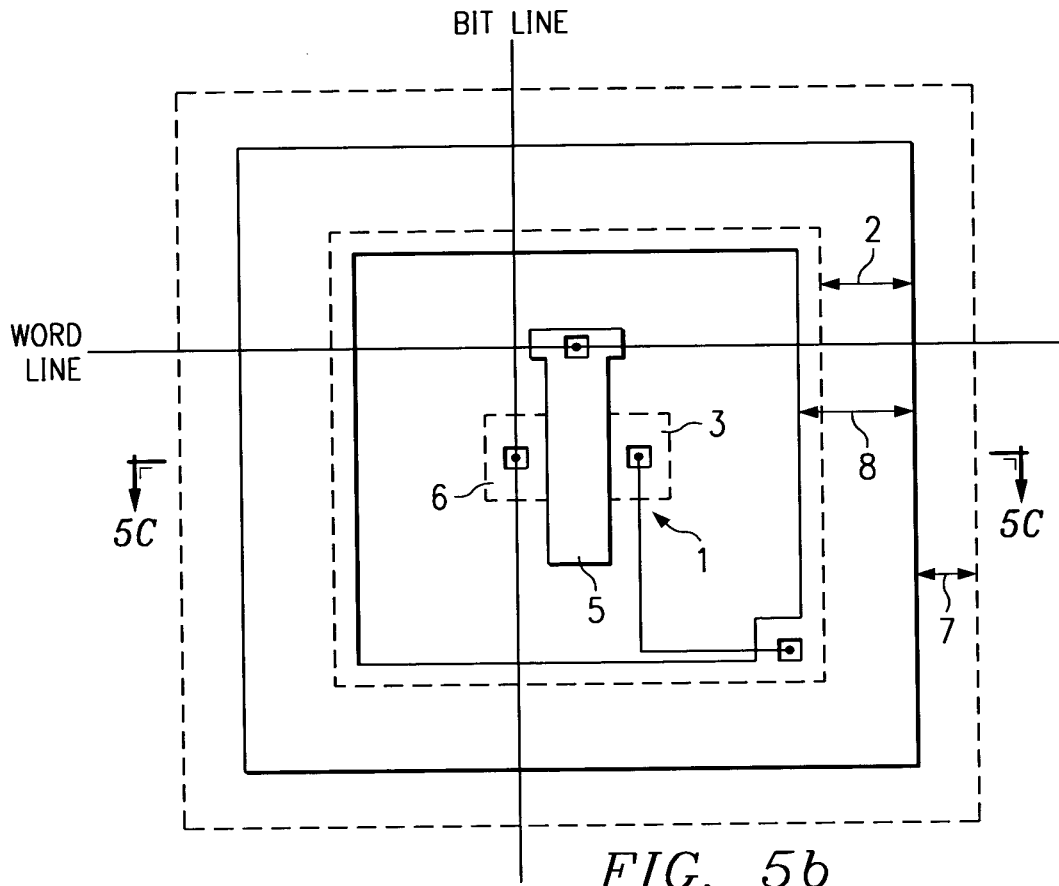


FIG. 5b

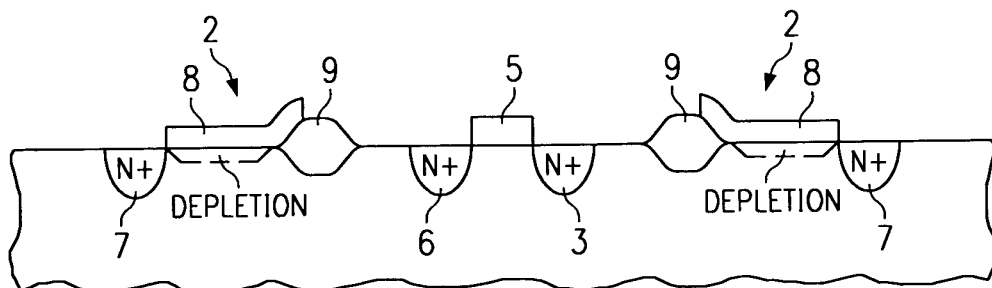
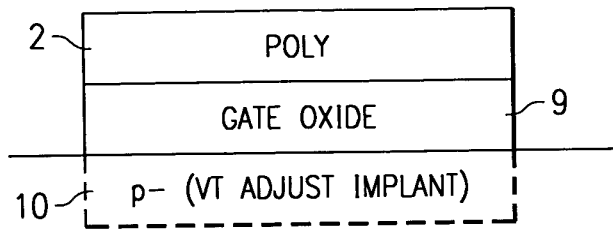


FIG. 5c

10034896 122801



(P) FIG. 6a

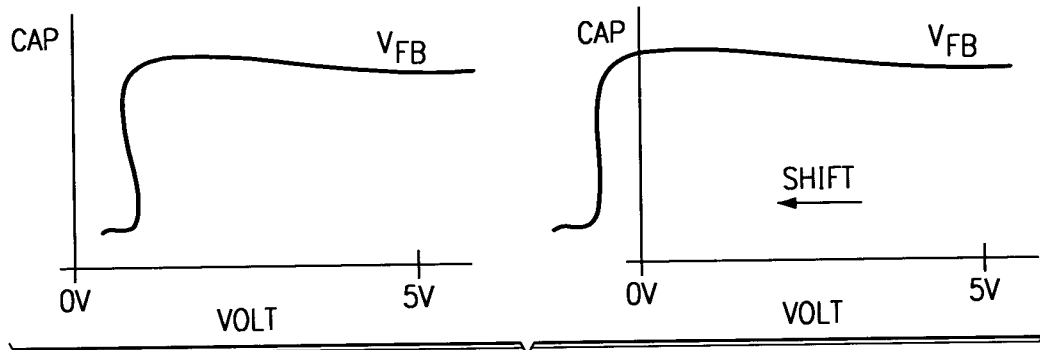


FIG. 6b

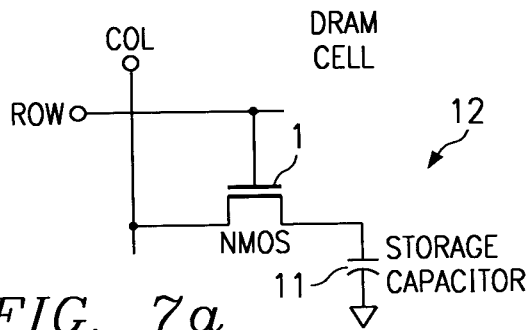


FIG. 7a

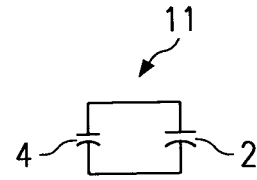


FIG. 7c

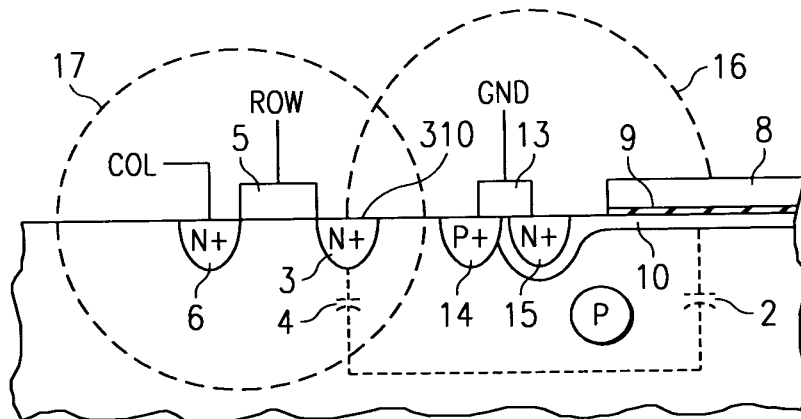


FIG. 7b

10034899-12801

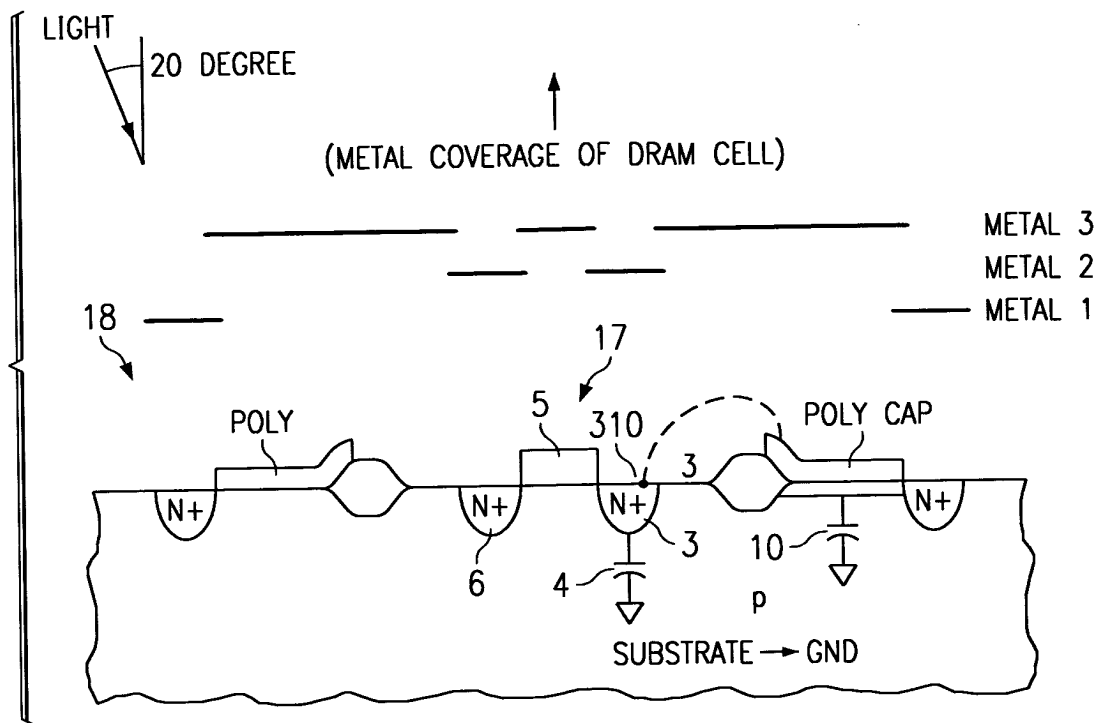


FIG. 8a

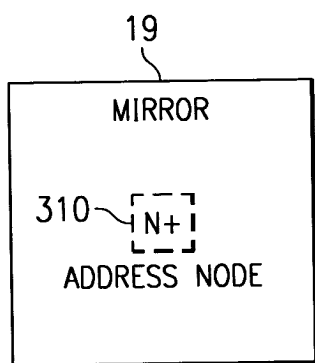


FIG. 8b

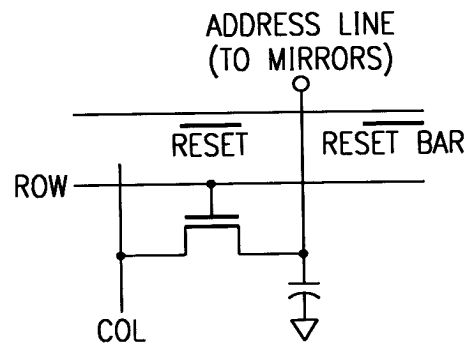


FIG. 9a

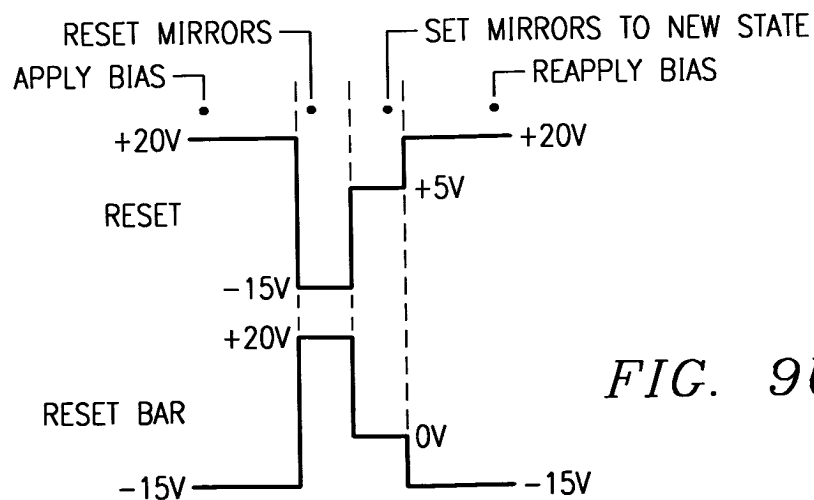


FIG. 9b

10034895-12301

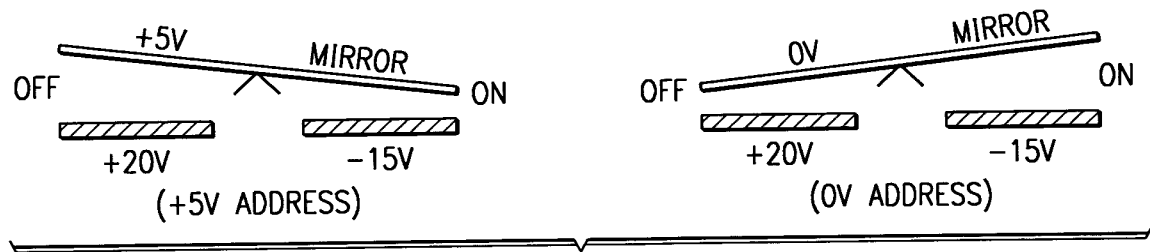


FIG. 9c

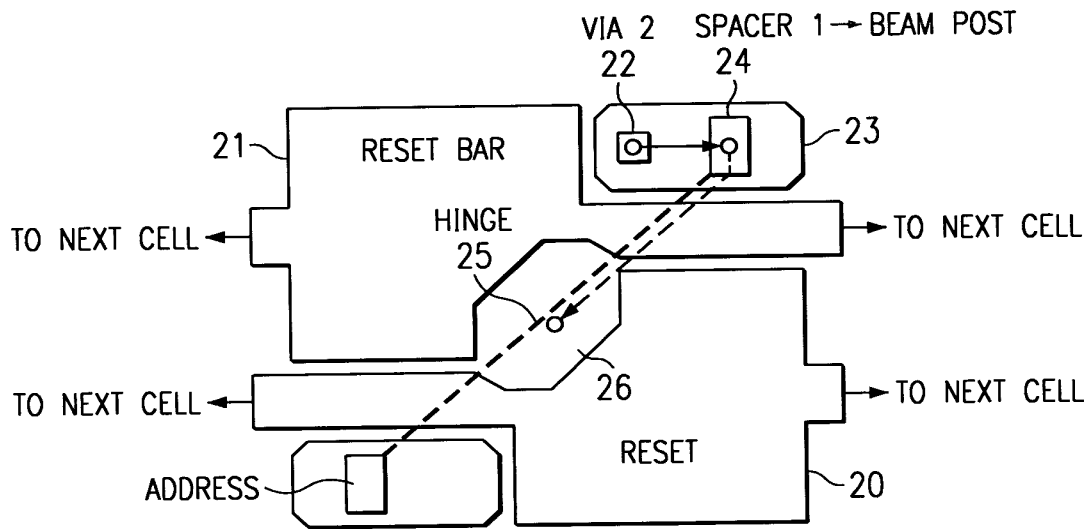


FIG. 10a

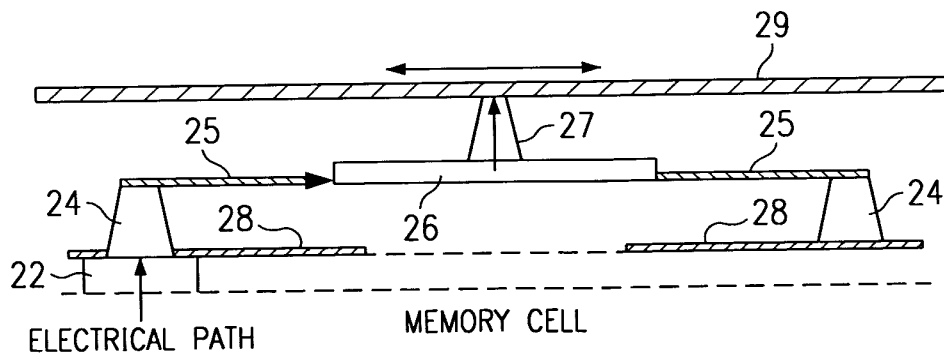


FIG. 10b

10034896-12801

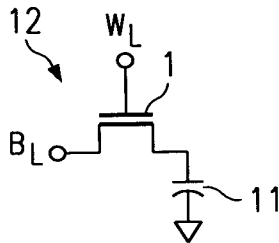


FIG. 11a

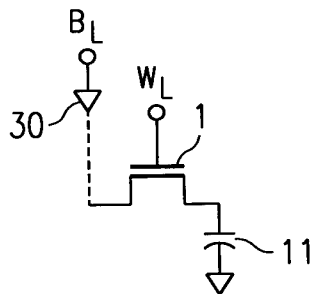
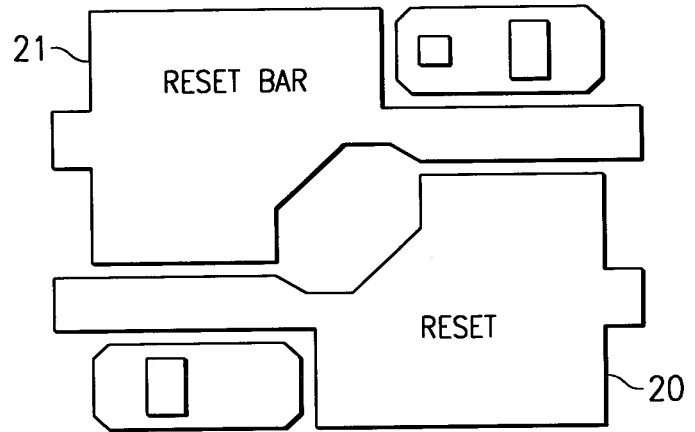


FIG. 11b

(ELECTROSTATIC MEMBER WHICH
ATTRACTS YOKE TO ON SIDE
FOR +5V ADDRESS VOLTAGE)



(ELECTROSTATIC MEMBER WHICH
ATTRACTS YOKE TO OFF SIDE
FOR 0V ADDRESS VOLTAGE)

FIG. 11c

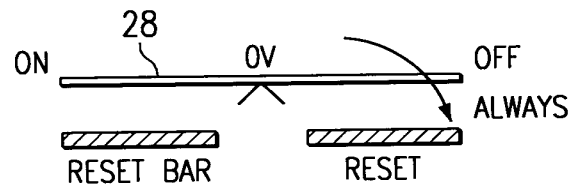


FIG. 11d

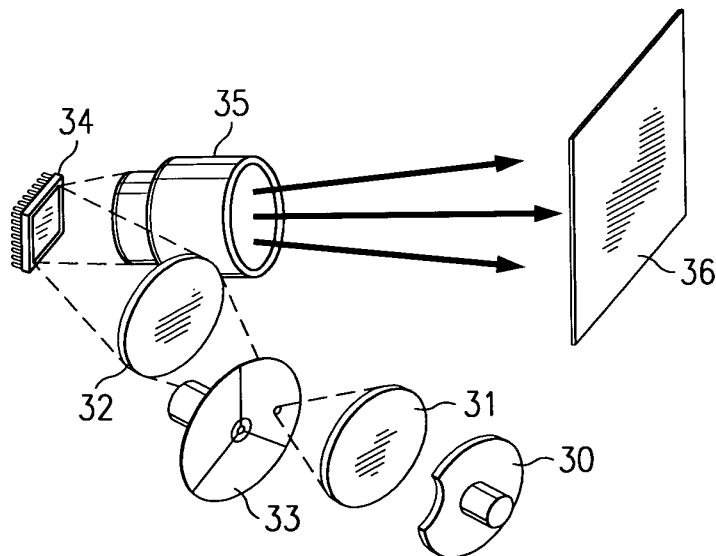


FIG. 12a

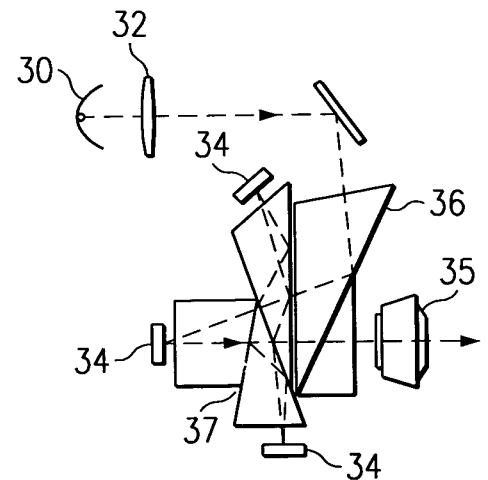


FIG. 12b

10034896-12001